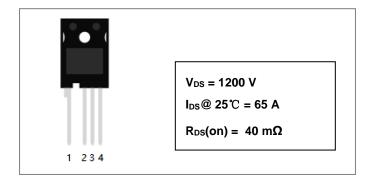




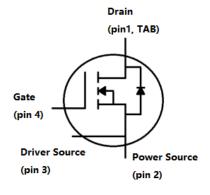
S3M0040120K 1200V SIC POWER MOSFET



Description

S3M0040120K is single SiC Power MOSFET packaged in TO-247-4 case. The device is a high voltage n-channel Enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S3M0040120K is ideal for energy sensitive, high frequency applications in challenging environments.

Circuit Diagram



Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = $40m\Omega$.
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin
- "-A" is an AEC-Q101 qualified device

Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)





Maximum Ratings (T_A = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Units	Note
Drain - Source Voltage	V _{DSmax}	$V_{GS} = 0 \text{ V}, I_{D} = 100 \mu\text{A}$			1200	V	
Gate - Source Voltage (dynamic)	V_{GSmax}	AC (f > 1 Hz)	-8		+22	V	
Gate - Source Voltage (static)	V _{GSop}	Static		-4 / +18		V	[1]
Continuous Drain Current	I _D	$V_{GS} = 18 \text{ V}, T_{C} = 25 ^{\circ}\text{C}$			65	A	
		V _{GS} = 18 V, T _C = 100 °C			46	_ ^	
Pulsed Drain Current	I _{D(pulse)}	Pulse width t _P limited by T _{jmax}			223	А	
Power Dissipation	P _D	T _C = 25 °C			130	W	

^[1] Recommended turn off gate voltage is -4 V. Recommended turn on gate voltage is 18 V. Do not use with V_{GSON} < 12 V.

Electrical Characteristics (T_A = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Units	
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_{D} = 100 \mu\text{A}$	1200			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 16$ mA	2	2.5	4	V	
		$V_{DS} = V_{GS}$, $I_{D} = 16$ mA, $T_{J} = 175$ °C		1.7		V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 1200 V, V _{GS} = 0 V		1	100	μΑ	
Gate Source Leakage Current	I _{GSS}	V _{GS} = 18 V, V _{DS} = 0 V		10	250	nA	
Drain Source On-State	Page 1	$V_{GS} = 18 \text{ V}, I_{D} = 40 \text{ A}$		40	52	mΩ	
Resistance	R _{DS(on)}	$V_{GS} = 18 \text{ V}, I_{D} = 40 \text{ A}, T_{J} = 175 ^{\circ}\text{C}$		50		mΩ	
Transconductance	gfs	$V_{DS} = 20 \text{ V}, I_{DS} = 40 \text{ A}$		18		S	
Transconductance		$V_{DS} = 20 \text{ V}, I_{DS} = 40 \text{ A}, T_{J} = 175 ^{\circ}\text{C}$		19		S	
Input Capacitance	Ciss	V _{GS} = 0 V		2844			
Output Capacitance	Coss	V _{DS} = 1000 V		134		pF	
Reverse Transfer Capacitance	C_{RSS}	V _{AC} = 25 mV		17			
Coss Stored Energy	Eoss	f = 1 MHz		78		μJ	
Turn-On Switching Energy	Eon	V _{DS} = 800 V, V _{GS} = -4 / 18 V		212			
Turn-Off Switching Energy	E _{OFF}	$I_D = 40 \text{ A}, R_{G(ext)} = 2.5 \Omega, L = 99 \text{ uH}$		198		μJ	
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 800 \text{ V}, V_{GS} = -4 / 18 \text{ V}$		12.5			
Rise Time	tr	$I_D=40$ A, $R_{G(ext)}=2.5$ Ω		14.7			
Turn-Off Delay Time	t _{d(off)}	Inductive Load Timing relative to		27.5		ns	
Fall Time	t _f	VDS Per IEC60747-8-4 pg 83		7.0		1	
Internal Gate Resistance	R _{G(int)}	f = 1 MHz, AC = 25 mV		1.3		Ω	
Gate to Source Charge	Q_{gs}	$V_{DS} = 800 \text{ V}, V_{GS} = -4 / 18 \text{ V}$		66			
Gate to Drain Charge	Q_{gd}	$I_D = 40 \text{ A}$		49		nC	
Total Gate Charge	Q_g	Per IEC60747-8-4 pg 21		143			

[•] China - Germany - Korea - Singapore - United States •

[•] http://www.smc-diodes.com - sales@ smc-diodes.com •





Reverse Diode Characteristics (T_A = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Тур.	Max.	Units
Diada Farward Valtaga	V_{SD}	V _{GS} = -4 V, I _{SD} = 20 A	4.3		V
Diode Forward Voltage	V _{SD}	V _{GS} = -4 V, I _{SD} = 20 A, T _J = 175°C	3.6		V
Continuous Diode Forward Current	Is	V _{GS} = -4 V, T _C = 25 °C	48		Α
Reverse Recovery Time	t _{rr}	V _{GS} = -4 V, I _{SD} = 40 A, T _J = 25 °C	16		ns
Reverse Recovery Charge	Qrr	$V_R = 800V$	221		nC
Peak Reverse Recovery Current	I _{mm}	dif / dt = 3000 A / µs	23		Α

Thermal-Mechanical Specifications

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T _{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R _θ JC	DC operation	0.31	°C/W

Ordering Information

Device	Package	Shipping
S3M0040120K	TO-247-4	30pcs/tube

Marking Diagram



Where XXXXX is YYWWL

S3M = Device Type

0040

= R_{DS}(on) = Reverse Voltage (1200V) 120

= Package SSG = SSG = Year $\mathsf{W}\mathsf{W}$ = Week = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

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Ratings and Characteristics Curves

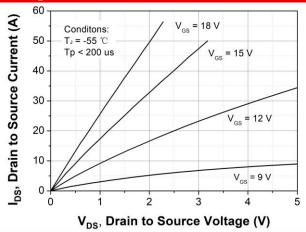


Figure 1. Output Characteristics T_J = -55 °C

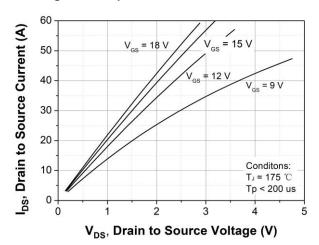


Figure 3. Output Characteristics T_J = 175 °C

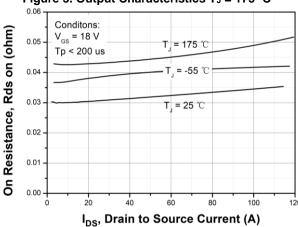


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

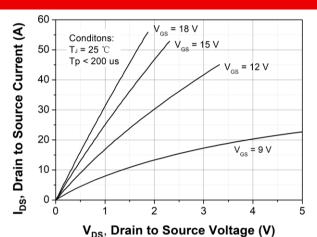


Figure 2. Output Characteristics T_J = 25 °C

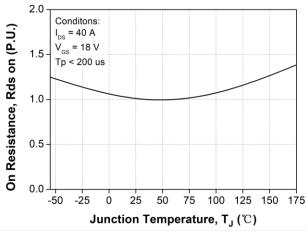


Figure 4. Normalized On-Resistance vs. Temperature

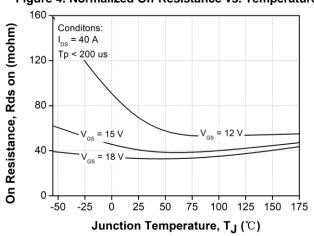


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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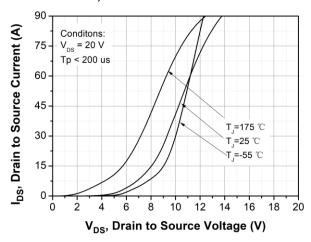


Figure 7. Transfer Characteristic for Various Junction Temperatures

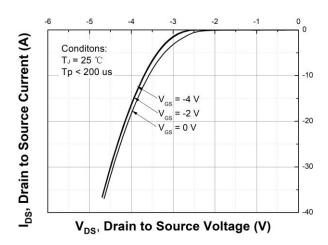


Figure 9. Body Diode Characteristic at T_J = 25 °C

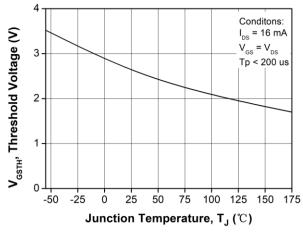


Figure 11. Threshold Voltage vs. Temperature

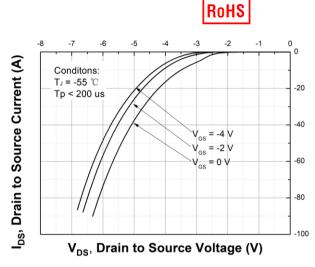


Figure 8. Body Diode Characteristic at T_J = -55 °C

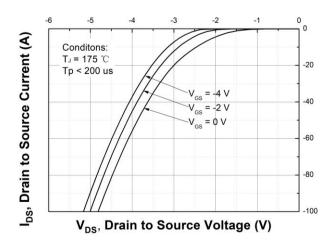


Figure 10. Body Diode Characteristic at T_J = 175 °C

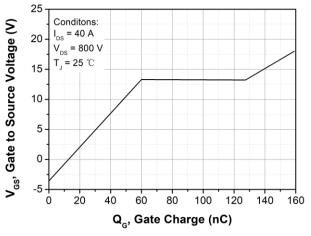


Figure 12. Gate Charge Characteristic

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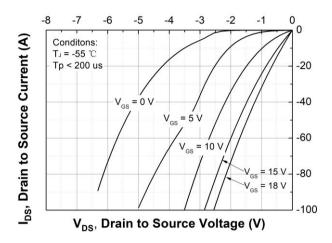


Figure 13. 3rd Quadrant Characteristic at T_J = -55 °C

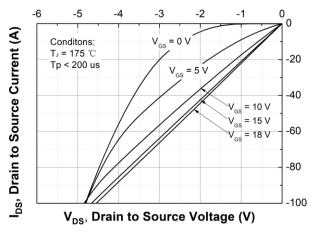


Figure 15. 3rd Quadrant Characteristic at T_J = 175 °C

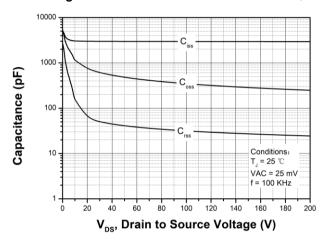


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)



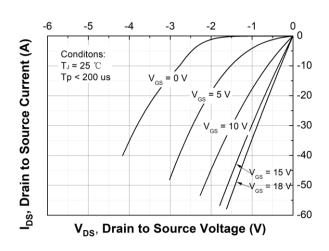


Figure 14. 3rd Quadrant Characteristic at T_J = 25 °C

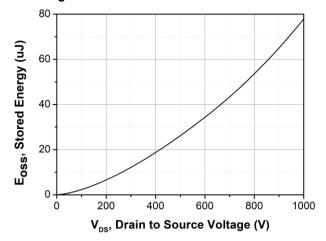


Figure 16. Output Capacitor Stored Energy

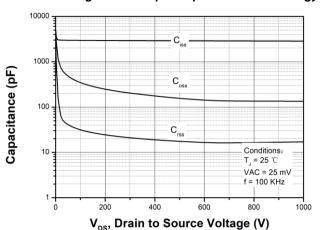


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

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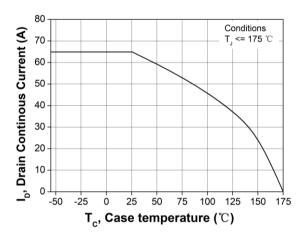


Figure 19. Continuous Drain Current Derating vs.

Case Temperature

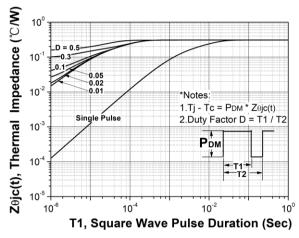


Figure 21. Transient Thermal Impedance (Junction - Case)

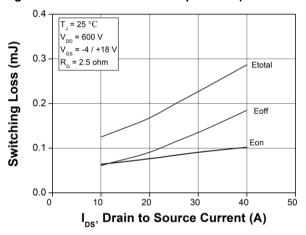


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600V$)



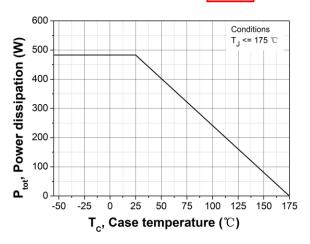


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

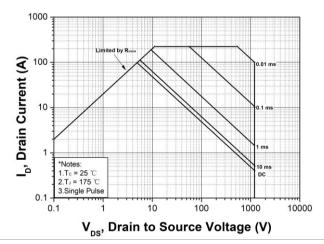


Figure 22. Safe Operating Area

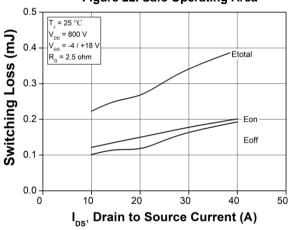


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V_{DD} = 800V)

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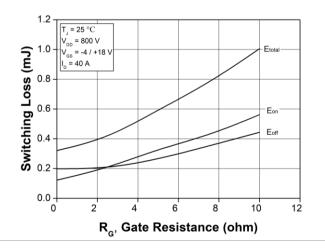


Figure 25. Clamped Inductive Switching Energy vs. R_{G(ext)}

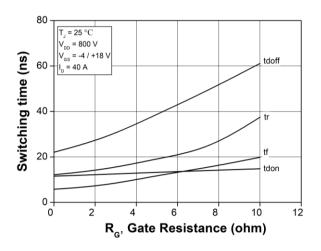


Figure 27. Switching Times vs. R_{G(ext)}



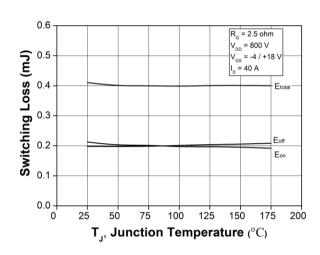


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

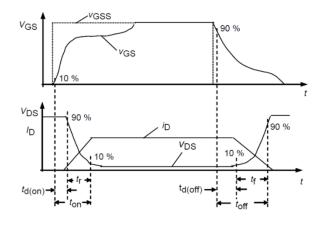
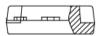


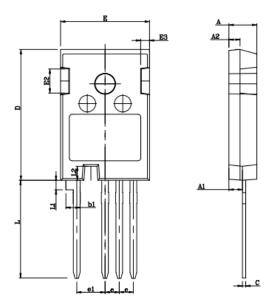
Figure 28. Switching Times Definition

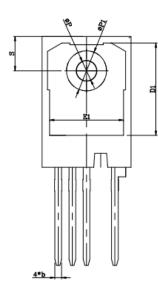




Mechanical Dimensions TO-247-4







SYMBOL	mm					
SIMBOL	Min	Nom	Max			
A	4.80	5.00	5.20			
A1	2.23	2.41	2.59			
A2	1.85	2.00	2.15			
b	1.11	1,21	1.36			
b1	2.35	2.55	2.75			
c	0.51	0.61	0.75			
D	23.30	23.45	23.60			
D1	16.25	16.55	16.85			
Е	15.75	15.94	16.10			
El	13.00	13.26	13.43			
E2	4.00	4.30	4.60			
E3	1.15	1.45	1.75			
e		2.54BSC				
el	5.08BSC					
L	17.31	17.47	17.82			
L1	1.50	1.70	1.90			
ØP	3.51	3.60	3.65			
ØP1	7.08	7.19	7.30			
S	6.15BSC					

S3M0040120K



Technical Data Data Sheet N2759, REV.-



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